

42390.P9006
MAR 22 2002
O I P E
PATENT & TRADEMARK OFFICE

COPY OF PAPERS
ORIGINALLY FILED

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Thomas D. Fletcher et al.

Serial No.: 09/823,098

Filing Date: March 29, 2001

For: METHOD AND APPRATUS FOR
CORRECTING A CLOCK DUTY
CYCLE

Examiner: Nguyen, Minh T.

Art Unit: 2816

Assistant Commissioner for Patents
United States Patent and Trademark Office
Washington, D.C. 20231

RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

In the Office Communication mailed February 11, 2002, the Examiner has
stated that restriction to one of the following inventions is required under 35

U.S.C. § 121:

- I. Claims 1-11, 18-21 and 28-30 considered to be drawn to a clock
distribution network.
- II. Claims 12-17 considered to be drawn to a circuit for correcting
clock duty cycle having a reset path and a feedback path.
- III. Claims 22-25 considered to be drawn to an apparatus for correcting
a clock duty cycle having smart buffer circuit.

2816
#6/Quatern
A
4/8/02
RECEIVED
MAR 27 2002
TECHNOLOGY CENTER 2800
Smith